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AMENDMENTS TO THE CLAIMS

Please amend the claims as indicated, below. Where claims are canceled, please cancel the claims without prejudice to their subsequent reinstatement in the present application or in a continuing application.

Listing of claims:

1-10 (Canceled)

11-14 (Canceled)

15. (Previously presented): A method of tuning a resistance of an integrated circuit (IC) comprising:

determining the resistance of the IC corresponding to a first configuration of parallel resistors, wherein a portion of the parallel resistors are enabled, at least one or more of the parallel resistors being disposed within the IC;

modifying the resistance of the IC by creating a second configuration of parallel resistors, wherein a different portion of the parallel resistors are enabled; and

wherein a desired impedance is provided by a combination of the resistance of the IC and an external terminating impedance external to the IC.

16. (Original): The method of claim 15 wherein the modifying the resistance is performed by writing to a register on the IC.

17. (Original): The method of claim 15 further comprising:

permanently disabling a subsequent modification of the second configuration of parallel resistors.

18. (Original): The method of claim 17 further comprising:

controlling the entire second configuration of parallel resistors to be enabled and disabled.

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19. (Original): The method of claim 17, wherein permanently disabling of a subsequent modification is achieved by blowing a fuse on the IC.

20. (Original): The method of claim 15, wherein modifying the resistance of the IC is performed by enabling a resistor of the parallel resistors to reduce the resistance of the IC by a professional percentage.

21. (Original): The method of claim 15, wherein modifying the resistance of the IC is performed by disabling a resistor of the parallel resistors to increase the resistance of the IC by a predetermined percentage.

22. (Previously presented): In a line interface having a programmable resistor, a method of matching an impedance of a transport medium comprising:

writing to a register that controls the programmable resistor, wherein the programmable resistor is disposed within an integrated circuit to couple to the transport medium; and

changing the programmable resistor to provide an effective impedance substantially matching the impedance of the transport medium responsive to writing to the register, wherein the effective impedance is provided by a combination of the programmable resistor and a terminating impedance external to the integrated circuit.

23. (Original): The method of claim 22 wherein changing the programmable resistor is accomplished by disabling the programmable resistor.

24. (Original): The method of claim 22 further comprising:
coupling the line interface to the transport medium.

25. (Original): The method of claim 24, wherein the transport medium supports a T1, J1, or E1 transport protocol.

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26. (Original): The method of claim 22 wherein the programmable resistor is changed to provide the effective impedance of 75 ohms, 100 ohms, or 120 ohms.

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